



Drafts

BRS:

Pending

Active

- L1: (790) chip adj bus
- L3: (7) 11 and 12
- L2: (30) virtual adj socket
- L4: (43) 11 adj2(interface or protocol)
- L5: (6661) bus near2(ic or semiconductor or chip or(integrated adj circuit))

Search List Browse Queue

DB: USPAT;US-PGPUB;EPO;JPO;DERWENT;IBM\_TDB

Default operator: OR

Phrases

☒ Highlight all terms initially

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1 BRS	L1	790	chip adj bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 10:11	
2 BRS	L3	7	11 and 12	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 10:12	
3 BRS	L2	30	virtual adj socket	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 10:39	
4 BRS	L4	43	11 adj2(interface or protocol)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 10:56	
5 BRS	L5	6661	bus near2(ic or semiconductor or chip or(integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:20	
6 BRS	L6	959	15 near5 interfacs3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 11:02	
7 BRS	L7	160673	function52 or logic or interface)adj2(block or module or unit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 11:07	
8 BRS	L8	3864	17 with(protocol or translat53 or proprietary)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 11:13	
9 BRS	L9	16	15 same 18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 11:22	
10 BRS	L10	2409	interfac53 near5(internal adj2 bus)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 11:22	
11 BRS	L11	15	110 same 18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 12:32	
12 BRS	L12	133	(110 and 18)not 111	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 12:51	
13 BRS	L13	1977	17 with(pipelins3 or(pipe adj lin53))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 12:53	
14 BRS	L14	108	113 same(bus adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 13:09	
15 BRS	L15	28	15 with 110	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 13:35	
16 BRS	L16	53	16 and 18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:03	
17 BRS	L17	1	"6128688".PN.	USPAT	2002/04/08 13:43	
18 BRS	L18	1	"6035364".PN.	USPAT	2002/04/08 13:45	
19 BRS	L19	1	"6018782".PN.	USPAT	2002/04/08 13:46	
20 BRS	L20	1	"5845107".PN.	USPAT	2002/04/08 13:50	
21 BRS	L21	5911	clock near5 ratio	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:03	
22 BRS	L22	13	112 and 121	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:05	
23 BRS	L23	0	116 and 121	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:05	
24 BRS	L24	2	115 and 121	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:10	
25 BRS	L25	73	16 and 121	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:10	
26 BRS	L26	387	(internal adj bus)near3(ic or semiconductor or chip or(integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:22	
27 BRS	L27	0	126 same 121	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:23	
28 BRS	L28	25	126 and 121	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:33	
29 BRS	L29	9735	((system or cpu or internal) near3 clock)near5 synchronis7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 16:42	
30 BRS	L30	27	129 with(bus adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:54	
31 BRS	L31	48	126 with(interfac53)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:24	
32 BRS	L32	577	full adj2 synchronis7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:31	
33 BRS	L33	29	132 with phase	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:25	
34 BRS	L34	425	full adj synchronis7	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:32	
35 BRS	L35	1	134 and 110	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:32	
36 BRS	L36	0	134 with(bus adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:55	
37 BRS	L37	0	134 same(bus adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:55	
38 BRS	L38	28	134 and(bus adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 18:55	
39 BRS	L39	13	110 and 716/53.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/08 19:21	

```

Drafts
  BRS:
  Pending
  Active
    L1: (1239) interfacs3 with
    L2: (115) 11 and 710/$3.c
    L3: (18475) pipelin$3 or(f
    L4: (26) 11 and 13
    L5: (67490) clock near4(s
    L6: (284638) (conver$4 or

```

☒ Highlight all hit terms initially

 BRS form
  IS&R form
  Image
  Text
  HTML

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	1239	interfac\$3 with proprietary	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 16:17
2	BRS	L2	115	11 and 710/\$3.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:32
3	BRS	L3	18475	pipelin\$3 or(pipe adj lin\$3)or stage)near4(block or module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 17:58
4	BRS	L4	26	11 and 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:11
5	BRS	L5	67490	clock near4(synchroni\$7 or resynchroni\$7 or ratio or domain)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:14
6	BRS	L6	284638	(conver\$4 or translat\$3 or proprietary or encod\$3 or decod\$3)near4 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:16
7	BRS	L7	36098	queue	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:16
8	BRS	L8	51823	bus near7 interfac\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:53
9	BRS	L9	1008	15 and 16 and 17 and 18	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:32
10	BRS	L10	241	19 and 710/\$3.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:32
11	BRS	L11	222	110 not 12	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:33
12	BRS	L12	20	13 and 111	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:53
13	BRS	L13	1166	((system or internal)adj bus)near4(ic or chip or(integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:18
14	BRS	L14	284	18 same 113	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 18:56
15	BRS	L15	229527	function\$2 or logic or interface)adj2(block or logic or unit or device	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:00
16	BRS	L16	193	114 and 115	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:00
17	BRS	L17	8	19 and 116	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:00
18	BRS	L18	474	(internal adj bus)near4(ic or chip or(integrated adj circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:19
19	BRS	L19	67	118 with interfac\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:19
20	BRS	L20	33	115 and 119	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:52
21	BRS	L21	34	119 not 120	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/04/05 19:52